Attorney Docket No.: 13248US02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

ESIN TERZIOGLU ET AL.

Serial No.: Not Assigned

Filed: December 5, 2003

For: BLOCK REDUNDANCY IMPLEMENTATION IN HEIRARCHICAL RAM'S

Examiner: Tan Nguyen

Group Art Unit: 2818

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Date : December 5, 2003

By: _____
Timothy L. Harney
Reg. No. 38,174

PRELIMINARY AMENDMENT

Mail Stop Patent Applications
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sirs:

Applicants submit this Preliminary Amendment in connection with the above newly filed application, which is a continuation of U.S. Application Serial No. 10/176,843 filed June 21, 2002, which is a continuation-in-part of, and claims benefit of and priority from, U.S. Application Serial No. 10/100,757 filed March 19, 2002, titled "Synchronous Controlled, Self-timed Local SRAM Block", now U.S. Patent No. 6,646,954 issued November 11, 2003, which is a continuation-in-part of U.S. Application Serial No. 09/775,701, filed February 2, 2001, now U.S. Patent No. 6,411,557, issued June 25, 2002, the complete subject matter of each of which is incorporated herein by reference in their entirety.

The **Amendments to the Specifications** begins on page 3.

The **Amendments to the Claims** begins on page 4.

The **Remarks** begin on page 8.